

FORM HDP-1449 (Based on Form PTO-1449)

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29250-001091/US

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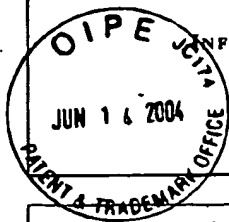
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Sheet 1 of 1



U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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217	PIP	197 25 275 A1	12/24/1998	Germany		English Abstract

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Ref. Desig.	Examiner's Initials	
217	PIP	BICKERSTAFF et al., "A 24 Mb/s Radix-4 LogMAP Turbo Decoder for 3GPP-HSDPA Mobile Wireless", February 11, 2003, pp. 1-3.
F	PIP	Vienlat, "Analog Convolutional Decoder, Project Thesis", May 2002.

Examiner:

P. Jean-Pierre

Date Considered:

4/28/04

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<p>FORM HDP-1449 (Based on Form PTO-1449)</p> <p>PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)</p> <p>Sheet 1 of 1</p>	ATTORNEY DOCKET No.	SERIAL No.
	29250-001091/US	NEW 10784814
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OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)		
Ref. Desig.	Examiner's Initials	
1	PIP	F. Lustenberger, M. Helfenstein, H.-A. Loeliger, F. Tarkoy, and G. S. Moschytz, "All-Analog Decoder for a Binary (18,9,5) Tail-Biting Trellis Code," in <i>Proc. of 25th European Solid-State Circuits Conference</i> , Duisburg, Germany, pp. 362-365, Sept. 1999.
	PIP	M. Moerz, T. Gabara, R. Yan, and J. Hagenauer, "An Analog 0.25 μ m BiCMOS Tailbiting MAP Decoder," in <i>Proc. IEEE International Solid-State Circuits Conference (ISSCC 2000)</i> , San Francisco, California, pp. 356-357, Feb. 2000.
	PIP	C. Winstead, J. Dai, W. J. Kim, S. Little, Y.B., Kim, C. Myers, C. Schlegel, "Analog MAP Decoder For (8,4) Hamming Code In Subthreshold CMOS," in <i>Proc. Advanced Research in VLSI</i> , Salt Lake City, Nevada, pp. 132-147, Mar. 2001.
	PIP	V. Gaudet and G. Gulak, "A 13.3Mb/s 0.35 μ m CMOS Analog Turbo Decoder IC With A Configurable Interleaver," in <i>Proc. IEEE International Solid-State Circuits Conference (ISSCC 2003)</i> , San Francisco, California, Feb. 2003.
	PIP	M. Moerz, "Analog Sliding Window Decoding," in <i>Proc. Joint Workshop of Communications and Coding</i> , Barolo, Italy, Nov. 2002.
	PIP	L.R. Bahl, J. Cocke, F. Jelinek and J. Raviv, "Optimal Decoding of Linear Codes for Minimizing Symbol Error Rate," <i>IEEE Trans. Inform. Theory</i> , vol. IT-20, pp. 284-287, Mar. 1974.
	PIP	J. Hagenauer, E. Offer, and L. Papke, "Iterative Decoding of Binary Block and Convolutional Codes," <i>IEEE Trans. Inform. Theory</i> , vol. 42, no. 2, pp. 429-445, Mar. 1996.
	PIP	J. B. Anderson and S. M. Hladik, "Tailbiting MAP Decoders," <i>IEEE Journal on Selected Areas in Communications</i> , vol. 16, no. 2, pp. 297-302, Feb. 1998.
	PIP	A. J. Viterbi, "An Intuitive Justification and a Simplified Version of the MAP Decoder for Convolutional Codes," <i>IEEE Journal on Selected Areas in Communications</i> , vol. 16, no. 2, pp. 260-264, Feb. 1998.
	PIP	S. Benedetto, D. Divsalar, G. Montorsi, and F. Pollara, "Soft-Output Decoding Algorithms for Continuous Decoding of Parallel Concatenated Convolutional Codes," in <i>Proc. ICC 96</i> , vol. 1, pp. 112-117, 1996.
	PIP	European Telecommunications Standards Institute, "Universal Mobile Telecommunications System (UMTS): Multiplexing and Channel Coding (FDD)," <i>SGPP TS 125.212 version 3.4.0</i> , pp. 14-20, Sep. 2000.

Examiner: R. Scantlin	Date Considered: 4/28/05
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